

**Research Grants(Rs. 5,50,000/-)**

Faculty	Project	Amount	Grant By
Mr. P.B. Mane Mrs. S. M. Jagdale	Implementation of PCI Bridge using FPGA	Rs. 3,00,000/-	University Of Pune
Mrs. S. T. Khot Mrs. S.A. Dhole	Minutia based fingerprint identification using Hough Transform	Rs. 1,50,000/-	University Of Pune
Mrs. V.S.Kashid Ms.S. D. Kale	DFT core generator	Rs.1,00,000/-	University Of Pune

**Papers Presented and Published in Journals:**

Sr. No.	Name of Journals	Volume and page no.	Date	Paper title	Author
1	JMET, New Delhi	Volume 1,Pg No.1	2009	Paper published on Implementation of Transparent PCI Bridge using FPGA	Mrs. S. M. Jagdale

**Papers Presented and Published in International Conferences/ Magzines :**

Sr. No.	Name of International Conference	Volume and page no.	Date	Paper title	Author
1	IMAPS India at Banglore		15-17 Dec 2008	Paper published on PCI Bus implementation using Verilog	Mrs. S. M. Jagdale
2	Computer Networks and Security at Pune,India		27-28 Sept 2008	Implementation of Protocol Anomaly Detection System for Network Security	Mrs. Sahana Bhosale
3	Emerging Trends in Engineering and Technology, Nagpur, India		July 2008	Analysis and Simulation of Noise in Power Line Communication	Mrs. Sahana Bhosale
4	Wireless, Mobile and Multimedia Networks, Mumbai, India.		January 2008	Conformance Testing of OSPF Protocol	Mrs. Sahana Bhosale
5	Wireless, Mobile and Multimedia Networks ,Mumbai, India.		January 2008	Architecture of a Single Sign On (SSO) for Internet Banking	Mrs. Sahana Bhosale

**Papers Presented and Published in National Conferences/ Seminars / Symposia :**

<b>Sr. No.</b>	<b>Name of National Conferences/Seminar/Symposia</b>	<b>Date</b>	<b>Paper title</b>	<b>Author</b>
1	National Conference on Computing ,Communication, Electronica and Management at Jalgaon	20 to 21/3/2009	Paper presented on Neural NetworkBased Handwritten Script Recognition System	Mrs.K.R.Choudhari
2	National Conference on Recent Trends in Computer Engineering'09 at Pune	29 to 31/12/2009	Paper presented on VHDL Implementation of Motion Estimation	Mrs.M.B.Gaikwad
3	National Conference on Wireless Technology at Pune	30/12/2008	Paper presented on FPGA Implementation of Reed Solomon Error Correcting Code	Mrs.P.P.Kale
4	National Conference on Signal Processing at Aurangabad	5 to 6/3/2009	Paper published on Reed Solomon Error Correcting Code using FPGA	Mrs.P.P.Kale