

**Bharati Vidyapeeth's College of Engineering for Women, Pune.**

**Electronics and TeleCommunication Department**

**Unit Test II B.E. Academic Year: 2010-2011**

**Subject: Embedded System Design**

**Duration: 1 hour**

**Marks: 30**

- 1) A) Explain task states & valid transitions with the help of suitable diagram (8)  
B) Explain necessary development tools for the development of embedded system (7)

OR

- 2) A) Compare the software architecture available for embedded system development (8)  
B) Discuss code optimization & its guidelines. (7)

- 3) A) What are different scheduling algorithms used in general? (8)  
B) What is RTOS. What is difference between traditional OS & RTOS (7)

OR

- 4) A) Explain in detail different types of semaphore. Explain inter process communication primitives mailbox, queues, pipes. (8)  
B) Explain RMA (4)  
C) Explain deadly embrace. (3)

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1) A) Explain common design metrics (8)

B) Explain IEEE 802.11 architecture (7)

OR

2) A) Differentiate between General Purpose System and Embedded System .What are recent trends in Embedded System Design (8)

B) What are the different categories in Embedded System (3)

C) Explain the communication protocol IrDA (4)

3) A) Explain the different types of ROM & RAM devices used in Embedded System (8)

B) Explain the hardware architecture of an Embedded System (7)

OR

4) A) With an example explain the factors to be considered while selecting a processor for an Embedded System (7)

B) Explain with example how share data problem occurs when interrupt arrives. What are different ways to avoid it? (8)