



Hon'ble Dr. Patangraoji Kadam Saheb
Founder, Bharati Vidyapeeth
Blessings



Bharati Vidyapeeth's College of Engineering for Women, Pune



e-newsletter



Hon'ble Dr. Patangraoji Kadam Saheb
Founder, Bharati Vidyapeeth

Participation of women in technology is an important aspect in social and economic development of the nation. It is a critical constituent in the process of improving the quality of life of women themselves. When women have economic empowerment, it is a way for others to see them as equal members of society. Through this, they achieve more self-respect and confidence by their contributions to their communities. As women play key roles in social transformation, Hon'ble Dr. Patangraoji Kadam Saheb established Bharati Vidyapeeth's College of Engineering for Women, Pune in June 2000 with the vision, "Women empowerment through Technical Education" and provided opportunity to women for higher education in the field of technology. The institute was started exclusively for women and it is running with 100% women students. Establishing and running Women Engineering College really contributes to social transformation through dynamic education which is the vision of Bharati Vidyapeeth.



Bharati Vidyapeeth's College of Engineering for Women, Pune

Pune-Satara Road, Dhankawadi, Pune 411043

Recognized by AICTE, New Delhi, DTE Mumbai, Affiliated to Savitribai Phule Pune University

Accredited by NAAC with "A" Grade

Id No.: PU/PN/Engg./150/2000, DTE College Code: EN6285

Phone: (020)24371684, (020)24361732 Fax: (020) 24372210

Email: coewpune@bharatividyaapeeth.edu, Website: <http://coewpune.bharatividyaapeeth.edu>

Undergraduate Programme

Sr. No.	Course	Intake	Course Code
1	B.E. Artificial Intelligence and Machine Learning (AI & ML)	60	628592150F
2	B.E. Computer Engineering (CE)	180	628524550F
3	B.E. Electronics and Telecommunication Engineering (E & TC)	120	628537250F
4	B.E. Information Technology (IT)	60	628524650F

Post Graduate Programme

Sr. No.	Course	Intake	Course Code
1	M.E. (Computer Engineering)	12	628524550F
2	M.E. (E & TC-VLSI & Embedded System)	9	628534150F

Research Centre

Course
Ph.D.(Doctoral Program in Electronics and Telecommunication Engineering)

Vision:

- Women Empowerment through Technical Education

Mission:

- Develop women students to rise to their full potential.
- Impart knowledge and prepare competent engineers.

Special Features:

- Best Engineering College with an All India Rank of 61 in THE WEEK-HANSA Research Survey 2024, securing All India Rank 35 among private engineering colleges and an impressive West Zone Rank 11.
- Received "Best Women College of the Year 2019" Award.
- Recipient of "College of Substance" Award.
- The oldest engineering college "exclusively for women".
- All government scholarships are applicable for eligible students.
- Placement opportunities in multinational companies with 100% assistance.
- Excellent university results and tradition of consistent university rank holders.
- MOUs with reputed industries and academia.
- On campus hostel facility with 24 x 7 security.
- DTE approved e-Scrutiny centre for admissions.

Facebook: <https://www.facebook.com/Bharati-Vidyapeeth-College-of-Engineering-for-Women-Pune-1599060517007121>

Instagram: https://instagram.com/bvcoew_pune?igshid=ep1a85ikhj6s





राष्ट्रीय मूल्यांकन एवं प्रत्यायन परिषद
विश्वविद्यालय अनुदान आयोग का स्वायत्त संस्थान
NATIONAL ASSESSMENT AND ACCREDITATION COUNCIL
An Autonomous Institution of the University Grants Commission

Certificate of Accreditation

*The Executive Committee of the
National Assessment and Accreditation Council
is pleased to declare
Bharati Vidyapeeth's
College of Engineering for Women
Dhankawadi, Tal. Haveli, Dist. Pune,
affiliated to Savitribai Phule Pune University, Maharashtra as
Accredited
with *CSPA* of 3.15 on four point scale
at *A* grade
valid up to October 24, 2029*

Date : October 25, 2024



Director

BC(SC)/222/1st Cycle/MHCOGN100226

Principal's Message



Prof. Dr. Pradeep V. Jadhav
Principal

Dear Students, Parents, and Stakeholders,

Warm Greetings from Bharati Vidyapeeth's College of Engineering for Women, Pune!

It gives us immense pride to present the latest edition of our e-newsletter "Blessings..." (Vol. 7, Issue 2) for the academic year 2024-25. This issue is especially significant as BVCOEW celebrates its Silver Jubilee - 25 years of excellence in technical education and women empowerment. We are proud to share that our institute has been reaccredited by NAAC with an 'A' grade, has applied for NBA accreditation, and actively participates in the NIRF ranking every year.

The newsletter highlights various academic and technical achievements of the semester, showcasing the vibrant campus life driven by our dedicated faculty and talented students. A major milestone this year includes the launch of a new UG program in Artificial Intelligence & Machine Learning with an intake of 60 students, and a PG program in Computer Engineering with 12 seats. We are also proud to be recognized as a Ph.D. Research Centre in Electronics and Telecommunication Engineering under SPPU.

Our first-year intake is now 420 students, reflecting the growing trust of the academic community in our institution. The issue also celebrates the dynamic student-led initiatives and technical events that continue to nurture innovation, leadership, and holistic development. Special thanks to Newsletter Coordinator Prof. Dr. Deepali Godse, Chief Editors, and the entire editorial team for their sincere efforts in curating this enriching edition.

Internal Quality Assurance Cell (IQAC)

IQAC Objectives:

- To imbibe quality environment at institute in all academic and administrative processes.
- To be instrumental in review of teaching learning process, structures, methodologies and student centric methods for achieving best educational environment.

Roles and responsibilities of IQAC:

- Keeping regular updates of NAAC and other quality improvement circulars.
- Conducting regular meetings of IQAC.
- Preparing Strategic plan of the institute.
- Preparation and submission of Annual Quality Assurance Report (AQAR) yearly.
- Maintaining academic records and conducting various audits at required intervals.
- Taking review of updating and updation of hardware and software requirements and internet facilities.
- Updating feedback forms as per guidelines from regulatory bodies.
- Providing guidelines for implementing ERP.
- Organizing various technical and nontechnical events.
- Preparation of reports of various activities for quality improvement.

Members List:

Sr. No.	Name of the IQAC Member	Designation	Position
1	Prof. Dr. P.V.Jadhav	Head of the Institute	Chairperson
2	Dr. K.D.Jadhav	Joint Secretary of Bharati Vidyapeeth	Member of Management
3	Dr. S. F. Patil	Executive Director of Bharati Vidyapeeth	Member of Management
4	Prof. Dr. S.R Patil	HOD, E & TC Engineering	Teacher Representative
5	Prof. Mrs. Khot S.T	Training cell Coordinator	Teacher Representative
6	Prof. Dr. V. R. Pawar	Academic & Research Coordinator	Teacher Representative
7	Prof. Dr. S. M. Rajbhoj	Industry institute Interaction	Teacher Representative
8	Prof. Ms. K.D.Mahajan	Alumni Coordinator	Teacher Representative
9	Prof. Mr. D. D. Pukale	HOD, Computer Engineering	Teacher Representative
10	Prof. Mrs. P. D. Kale	Placement cell Coordinator	Teacher Representative
11	Prof. Dr. D. A. Godse	HOD, Information Technology	Teacher Representative
12	Prof. Dr. K. A. Malgi	ICT & IT Infrastructure Coordinator	Teacher Representative
13	Prof. Dr. A. M. Pawar	HOD, Engineering Sciences and Allied Engineering	Teacher Representative
14	Mrs. Vaishali Kadam	Office Superintendent	Admin. Representative
15	Dr. V.M. Mohite	Librarian	Admin. Representative
16	Mr.Nityanand Prabhutendolkar	Chief Technical Officer, Ergen Technovation Pvt. Ltd.	Industry Representative
17	Mr. Sanjaykumar Gupta	Parent	Parent Representative
18	Ms. Shital Patil	Alumna (IT)	Alumni Representative
19	Ms. Khushi Mittal	Student (E & TC)	Student Representative
20	Prof. Dr. S. S. Chorage	Professor (E & TC)	Coordinator of the IQAC

From the Desk of Coordinator...



Prof. Dr. Deepali A. Godse
Newsletter Coordinator

Dear Stakeholders,

Greetings!

As shared in our previous e-newsletter, our institute has been accredited with an “A” grade in NAAC Cycle 2, an important milestone coinciding with our Silver Jubilee Year. This achievement reflects the dedication of our students, faculty, and staff, along with the unwavering support of our management and stakeholders. Guided by our vision of “Women Empowerment through Technical Education,” we are now progressing toward NBA accreditation, confident that the blessings of our revered founder, Hon. Dr. Patangraoji Kadam Sahab, will continue to inspire us.

The semester was marked by impactful activities, including an International Conference, symposia, and an IIT-sponsored workshop, fostering a culture of innovation and research. We are now preparing for our second International Conference, focusing on enhancing research involvement among students and faculty. In alignment with the National Education Policy (NEP), the revised second-year curriculum has been implemented by Savitribai Phule Pune University.

We celebrated International Women’s Day through INOAug APEX UTSav 2025 and hosted sports competitions, a technical festival, and our grand annual gathering, BharatiYugam 2025, each with great zeal and distinguished guests.

This edition of our newsletter captures highlights from these events. I sincerely thank our respected Principal, Prof. Dr. Pradeep Jadhav; Heads of Departments; dedicated faculty and staff; and enthusiastic students for their contributions. I also extend appreciation to the editorial team for their meticulous efforts.

Finally, we are truly grateful to each of you, our stakeholders, for standing by us through our 25-year journey. We look forward to your continued partnership as we strive for sustained progress and excellence in the years ahead.



Department of Electronics and Telecommunication Engineering

Vision

To develop women professionals to become a valuable resource for industry and society through E&TC Engineering.

Mission

- To provide quality and value based education for women in the field of E&TC Engineering.
- To train women to keep pace with rapidly changing technological needs of industry and research.


Program Educational Objectives (PEOs)

1. Ability to apply electronics knowledge, to identify formulates and solve Engineering problems.
2. *Acquire knowledge to find out workable solutions in the field of Telecommunication.*
3. *Exhibit programming skills with the use of various software tools.*
4. *Inculcate continuous learning through interdisciplinary approach*

Program Outcomes (POs)

On completion of the program graduate will be able to

1. **Engineering knowledge:** *Apply the knowledge of mathematics, science, engineering fundamentals, and an engineering specialization to the solution of complex engineering problems.*
2. **Problem analysis:** *Identify, formulate, review research literature, and analyse complex engineering problems reaching substantiated conclusions using first principles of mathematics, natural sciences, and engineering sciences.*
3. **Design/development of solutions:** *Design solutions for complex engineering problems and: design system components or processes that meet the specified needs with appropriate consideration for the public health and safety, and the cultural, societal, and environmental considerations*

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4. **Conduct investigations of complex problems:** Use research-based knowledge and research methods including design of experiments, analysis and interpretation of data, and synthesis of the information to provide valid conclusions
 5. **Modern tool usage:** Create, select, and apply appropriate techniques, resources, and modern engineering and IT tools including prediction and modelling to complex engineering activities with an understanding of the limitations
 6. **The engineer and society:** Apply reasoning informed by the contextual knowledge to assess societal, health, safety, legal and cultural issues and the consequent responsibilities relevant to the professional engineering practice
 7. **Ethics:** Apply ethical principles and commit to professional ethics and responsibilities and norms of the engineering practice
 8. **Environment and sustainability:** Understand the impact of the professional engineering solutions in societal and environmental contexts, and demonstrate the knowledge of, and need for sustainable development.
 9. **Individual and teamwork:** Function effectively as an individual, and as a member or leader in diverse teams, and in multidisciplinary settings.
 10. **Communication:** Communicate effectively on complex engineering activities with the engineering community and with society at large, such as, being able to comprehend and write effective reports and design documentation, make effective presentations, and give and receive clear instructions.
 11. **Project management and finance:** Demonstrate knowledge and understanding of the engineering and management principles and apply these to one's own work, as a member and leader in a team, to manage projects and in multidisciplinary environments.
 12. **Life-long learning:** Recognize the need for and have the preparation and ability to engage in independent and life-long learning in the broadest context of technological change.

Program Specific Outcomes (PSOs)

The graduate will be able to

1. Give techniques and solutions by using acquired knowledge and skills.
2. Design and develop Electronics & and telecommunication-based systems.
3. Create, select and adapt techniques, resources and tools with understanding of associated limitations.
4. Identify and address their own needs in the changing world through lifelong learning.

HOD's Message



Prof. Dr. S. R. Patil

Head of Electronics and Telecommunication Engineering Department

It is with great pride and enthusiasm that I welcome you to the latest edition of our Department of Electronics and Telecommunication Engineering's e-newsletter. This platform continues to be a vibrant reflection of our department's unwavering commitment to academic excellence, innovation, and collaborative growth. This newsletter showcased here is the result of the collective efforts of our dedicated faculty, industrious students, and ever-supportive staff. Together, we have cultivated a culture that values continuous learning, cutting-edge research, and forward-thinking solutions to meet the demands of an increasingly dynamic global tech landscape.

At the heart of our vision lies the empowerment of our girl students—not just with theoretical knowledge, but with hands-on skills, problem-solving abilities, and the ability to adapt to the latest technological trends such as 5G, IoT, AI, and sustainable electronics. Through strong industry collaborations, interdisciplinary projects, and a focus on experiential learning, we are preparing tomorrow's engineers to not only meet the future, but to shape it.

Our department is also making significant strides in fostering an innovation-driven mindset through initiatives such as startup incubation support, technical clubs, and national-level competitions. We encourage students to think beyond textbooks and contribute meaningfully to real-world challenges. Faculty members are actively engaged in publishing quality research, securing grants, and mentoring students in cutting-edge domains. These efforts are building a strong foundation for a research-oriented and industry-aligned academic environment. Moreover, we are deeply invested in promoting inclusivity, ethical practices, and a culture of lifelong learning. By integrating value-based education with technical excellence, we aim to produce not just skilled engineers, but responsible global citizens.

This newsletter is a celebration of that synergy—a chronicle of our shared journey and aspirations.

I encourage you to explore this edition and discover the milestones, innovations, and inspiring stories that define who we are. Let us continue working together to build a resilient, resourceful, and revolutionary future. To conclude, I leave you with a thought-provoking proverb by prominent Indian Hindu philosopher and spiritual leader, *Swami Vivekananda* that reflects the spirit of perseverance and vision: ***"Education is the manifestation of the perfection already in man."***

ANNUAL TECHNICAL FESTIVAL: “BharatiYugam 2024-25”



Bharati Vidyapeeth’s College of Engineering for Women, Pune organized its Annual Technical Festival, “BharatiYugam 2025 – Transforming Dreams into Reality,” on 8th April 2025.

Bharati Vidyapeeth’s College of Engineering for Women, Pune, proudly celebrated its annual technical festival BharatiYugam 2025, marking a significant milestone in its Silver Jubilee Year. Centered around the inspiring theme "Transform Dreams into Reality", the event served as a dynamic platform for students and tech enthusiasts to turn visionary ideas into impactful innovations.

The inaugural ceremony was graced by distinguished dignitaries, including Chief Guest Hon’ble Dr. Asmita Tai Jagtap, Executive Director, Bharati Vidyapeeth Medical Foundation. The ceremony was further honored by the presence of Guests of Honor: Hon’ble Dr. Rajendrakumar Sharma (Director, Spel Technologies Pvt. Ltd.), Hon’ble Dr. Yogesh Pawar (Founder and Managing Partner, School of Inspirational Leadership), and Hon’ble Komal Machindar (Head, Delivery Excellence, LTIMindtree). The institutional leadership was represented by Principal Prof. Dr. P.V. Jadhav, Vice Principals Prof. Dr. S.S. Chorage and Prof. Dr. A.M. Pawar, whose presence added depth to the occasion.

The festival was convened by Prof. D. D. Pukale, with Prof. K. L. Patil and Prof. V. D. Kulkarni as Coordinators. Ms. Aishwarya Bhansali effectively led as the Student Coordinator, contributing to the event’s seamless execution.

BharatiYugam 2025 offered a vibrant blend of technical and non-technical events, ranging from Project Exhibition, Paper Presentation, Startup Idea Competition, Robo Race, Coding Competition, to Poster Presentation and Technical Quiz. On the creative front, Rangoli, Sketch Competition, Tote Bag Painting, and a special Photo Exhibition for faculty and staff infused cultural vibrancy into the atmosphere. With an overwhelming response of 342 student registrations from various colleges, the fest witnessed wide participation, highlighting its growing reputation. The synergy of meticulous planning, enthusiastic participation, and the unwavering support of faculty and staff under the visionary leadership of Principal Prof. Dr. Pradeep Jadhav truly embodied the spirit of “Teamwork makes the dream work”. BharatiYugam 2025 concluded on a high note, leaving behind a legacy of innovation, collaboration, and excellence—an inspiration for future endeavours.

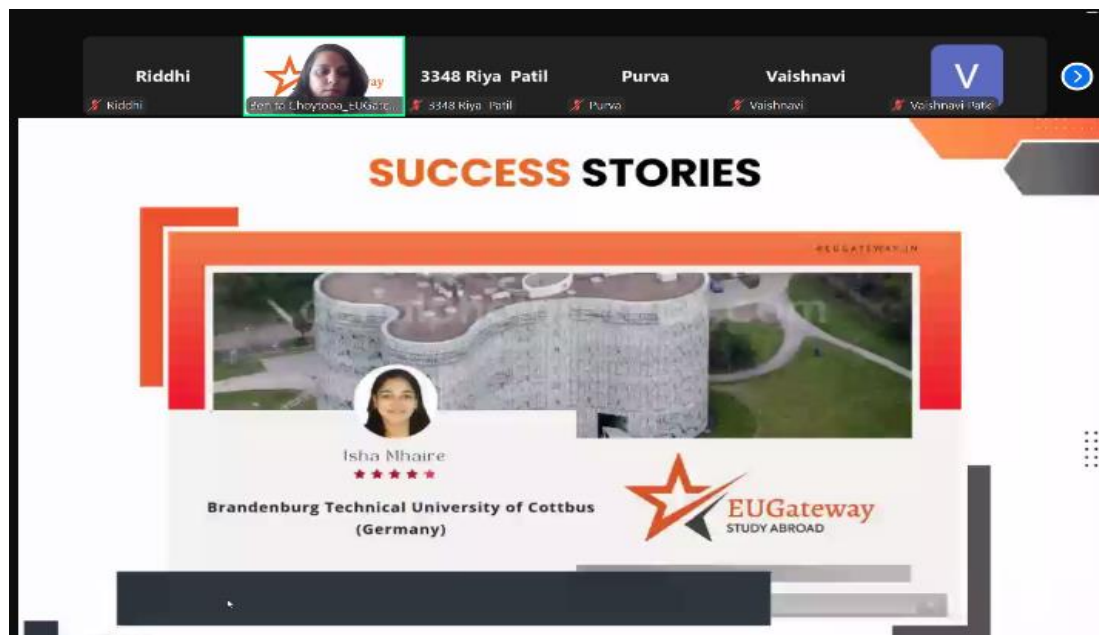
Institute level Activities



National Conference on “Interdisciplinary Advancements in Emerging Technologies” (IAET-2025) in presence of Hon’ble Dr. Rajendrakumar Sharma (Director, Spel Technologies Pvt. Ltd.), on Tuesday, 8th April , 2025



One-Day Symposium on “Oracle APEX Certification” with Guests of Honour Ms. Richard Dalvi, Mr. Santosh Kumar, Mr. Bharat Bhatia and Mr. Anuj Bhardwaj on 8th March 2025, featuring Women’s Day celebration



Seminar on “Importance of Profile Building and Career Mapping”, by Benita Albert organized under Career Guidance Cell



Awareness Session on “KAPILA” by Prof. Dr. Sharada Kore, Institute KAPILA Nodal Officer, for faculty and KAPILA club students organized by IPR Cell



Seminar on “Higher Studies in India and Abroad”, by Mr. Ranjit Kalangutkar, guiding students on academic opportunities and pathways post-graduation organized under Career Guidance Cell



Two days’ workshop on “Simulation and Modelling Using Python” for first-year students by Mr. Santosh Yadav, CADD Career organized by Institution Innovation Council Cell



Participation in “The SheInspire Hackathon 2025” organized by Zensar, a global IT Services Company, aimed at bringing together female developers, designers, and tech enthusiasts to collaborate on solving real-world problems through innovative solutions under Placement Cell



Two Days Workshop on Start-up and Self-Defence by Amol Nitave, CEO & Founder EvolvingX on 7th and 8th March 2025 under Training Cell

Major Technical Activity Workshop on “Skill Development: PCB Making and Power Supply Design”



Workshop on “Skill Development: PCB Making and Power Supply Design” by Mr. Shubham Ubale and Mr. Mahesh Adsule(Tronix 365) from 20th February to 22nd February 2025 for SE E&TC students.

A three-day hands-on workshop on Skill Development was organized from 20th to 22nd February 2025 under the guidance of the Board of Student Development, Savitribai Phule Pune University (SPPU) for Electronics and Telecommunication Engineering department students. Principal, Prof. Dr. P.V. Jadhav welcomed the speaker for the session and encouraged students to actively participate in this workshop. The primary objective was to equip students with practical knowledge in Printed Circuit Board (PCB) making and Power Supply design, while also encouraging eco-friendly practices through e-scrap utilization and recycling techniques.

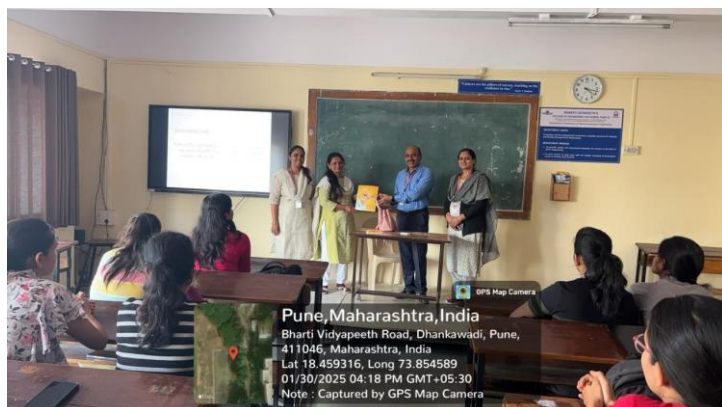
The sessions were conducted by Mr. Shubham Ubale and Mr. Mahesh Adsule, both experienced R&D engineers in Tronix 365, with support from a team of 12 trainees. The workshop offered step-by-step hands-on training in circuit designing, etching, soldering, and assembling PCBs. Students were introduced to circuit design software, guided through the design process, and given the opportunity to fabricate and test their own circuits. A unique highlight of the workshop was the focus on utilizing electronic waste. Sessions on power supply design included practical insights into circuit stability, voltage regulation, and real-world applications.

Students were also trained in troubleshooting techniques, circuit testing, and optimization. The workshop included interactive Q&A sessions to address student queries and provide expert guidance.

As a result, participants gained industry-relevant technical skills, improved their understanding of electronics, and developed confidence in handling real-world projects. The initiative bridged the gap between theory and practice while also promoting environmental responsibility.

The enthusiastic participation and positive feedback reflected the workshop's success. It proved to be a valuable step towards preparing students for careers in electronics and electrical engineering, with a focus on innovation and sustainability. HOD, Prof. Dr. S. R. Patil congratulated coordinator of workshop, Prof. K. R. Chaudhari as this workshop was an excellent initiative to equip students with practical skills in electronics while fostering awareness about sustainable engineering practices through e-waste utilization.

Technical Activities



BE: Seminar on “ Career Opportunities in Biomedical Engineering Field ” by Mrs. Vaishnavi Banke (Medi factsinc, Pune) on 30th January 2025.



TE: Seminar on “ Bridging microcontrollers and the cloud: A hands-on one day workshop on IoT, Automation and AI ” by Mr. Santosh Yadav (CADD CAREER, Pune) on 20th March 2025.



SE : Industrial Visit at Katraj Milk Dairy, Pune on 21st February 2025.

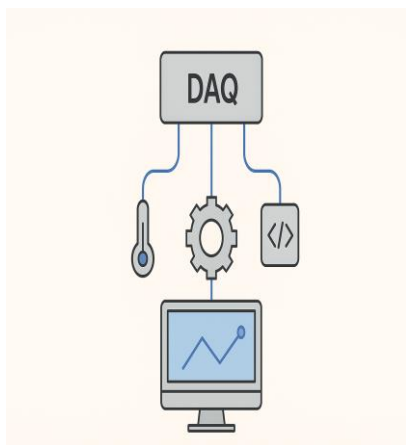


BE,TE : Industrial Visit at Seimens Kalwa Factory, Thane on 3rd June 2025.



Harnessing the Power of Data Acquisition in Engineering

In the present era of rapid technological advancement, engineering excellence is increasingly defined by the ability to collect, interpret, and apply data effectively. *Data acquisition* (DAQ), the systematic process of gathering and analyzing information from physical systems through sensors, instruments, and computers, stands at the heart of this transformation. It is not simply a technical process, but a bridge between theoretical knowledge and practical implementation, enabling engineers to make well-informed, real-time decisions. The applications of DAQ are vast and continue to expand. In the automotive sector, it allows for precise measurement of vehicle performance, fuel efficiency, and emission levels. In civil engineering, structural health monitoring systems use DAQ to ensure the safety of bridges, dams, and buildings by detecting stress or vibration patterns early. In biomedical fields, patient monitoring devices rely heavily on DAQ to provide accurate and continuous data about vital signs. Industrial automation, too, depends on reliable data streams for



predictive maintenance, quality control, and process optimization. Each of these applications highlights how DAQ contributes to safety, sustainability, and innovation. For employers, engineers who demonstrate proficiency in data acquisition are highly valued. Mastery of DAQ hardware, software platforms, and analytical tools reflects not only technical competence but also problem-solving ability and adaptability. In an age where industries generate vast volumes of information, the ability to filter, interpret, and apply relevant data effectively makes a professional stand out. Moreover, familiarity with emerging technologies—such as cloud computing, machine learning, and the Internet of Things—enhances the relevance of DAQ in designing intelligent, connected systems. We strongly encourage young engineers to invest time in developing practical skills in this field. Hands-on exposure to sensors, data loggers, and analysis software builds confidence and strengthens professional

readiness. Equally important is cultivating the habit of continuous learning, since DAQ technologies are evolving rapidly. By staying informed and adaptable, engineers can ensure their expertise remains future-proof. As an organization, we view data acquisition not only as a tool for operational efficiency but as a driver of innovation and growth. By harnessing its potential, engineers can design smarter products, create safer environments, and contribute to a more sustainable world. The message is clear: data is power, and those who know how to acquire and use it responsibly will shape the future of engineering.



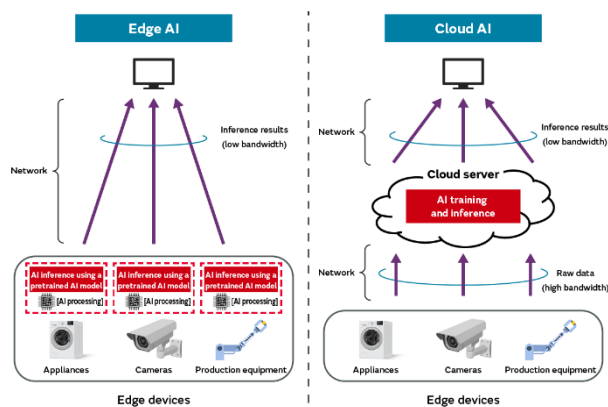
Prof. Rakesh Nalawade
Chief Technology Officer,
Aashay Measurements Private
Limited,
Warje, Pune

Edge AI: Powering Smart Devices with Local Intelligence

In the age of AI, where cloud computing once dominated, a new and revolutionary concept is taking over—**Edge AI**. This cutting-edge technology brings the power of Artificial Intelligence directly to the device where data is generated, such as smartphones, cameras, wearable, and drones. By eliminating the need for constant cloud interaction, Edge AI allows real-time decision-making, enhanced privacy, and increased reliability. This innovation is rapidly transforming industries ranging from healthcare and agriculture to transportation and consumer electronics. Edge AI refers to the deployment of artificial intelligence algorithms directly on edge devices—those located physically close to the source of data. These devices can independently process and respond to information without relying on cloud servers, thus ensuring faster performance, reduced latency, and enhanced data privacy. Traditional AI applications rely heavily on cloud computing, resulting in latency, privacy concerns, and bandwidth limitations. Edge AI overcomes these drawbacks by embedding AI processing power into local hardware, such as Smartphones and IoT devices with built-in AI chips, Embedded systems like Raspberry Pi or NVIDIA Jetson and Smart cameras that perform object detection in real-time. By bringing intelligence closer to where data is generated, Edge AI enables more efficient, autonomous systems. Edge AI is driven by the convergence of hardware and software advancements like:

- **Edge AI Chips:** Qualcomm AI Engine, Apple Neural Engine, Intel Movidius.
- **Lightweight Frameworks:** TensorFlow Lite, Open VINO, PyTorch Mobile.
- **Low-power Hardware:** Devices like Google Coral and NVIDIA Jetson Nano support AI on the edge with minimal energy consumption.

Edge AI is reshaping the way machines interact with the world in different applications such as Healthcare, Agriculture, Smart Cities, Consumer Electronics and Autonomous Vehicles etc. However, with the integration of 5G and upcoming 6G, the capabilities of Edge AI are set to expand dramatically, enabling faster and more secure operations. Edge AI represents the next leap in intelligent computing—empowering devices to think and act locally. It offers speed, security, and efficiency in a connected world where instant decision-making is crucial. As Edge AI continues to evolve, it promises to revolutionize every sector it touches, offering smarter solutions that work in real-time, with minimal external support.

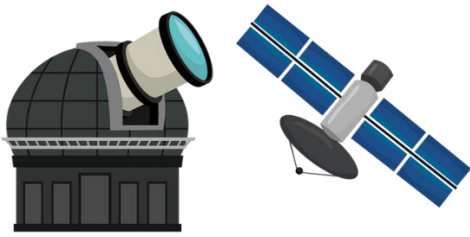


Prof. Pankaj S. Bhokare
Principal,
Navsahyadri Education society's
Group Of Institutions (Polytechnic),
Pune
(Parent of Ms. Prerna Bhokare, SE
E&TC)

STeRG aboard POEM-4 - A space exploration collaboration of MIT-WPU & ISRO

The Indian Space Research Organisation (ISRO) continues to drive scientific innovation and accessible space development through its Polar Satellite Launch Vehicle (PSLV) Orbital Experimental Module—POEM. This initiative repurposes the PSLV's fourth stage as an experimental satellite bus, enabling institutions and researchers to focus solely on payload development, thus making innovation more inclusive.

In space missions, a payload refers to the scientific or technological instruments carried aboard a satellite. These instruments are used for various purposes such as experimentation, testing, and studying space environments like microgravity. POEM projects have so far supported explorations in microbiology, vegetation growth, space robotics, propulsion, radiation shielding, and AI in space, among others. By offering a ride to space, POEM opens doors for universities, start-ups, and research bodies to bring their ideas into orbit.



Among the many contributors to POEM-4 (part of the PSLV-C60 / SpaDeX Mission) is the Space Technology Research Group (STeRG) at MIT World Peace University (MIT-WPU). This team, consisting of B.Tech students and faculty from the Department of Electrical and

Electronics Engineering (DoEEE), first made their mark in the CANSAT India Student Competition—a national event promoting hands-on space science education. Organised by the Aeronautical Society of India (ASI) and In-SPaCe under the Make in India and Atmanirbhar Bharat initiatives, the competition required participants to develop a CAN-sized satellite launched to an altitude of 800–900 m. Building on this momentum, STeRG signed an MoU with ISRO via In-SPaCe and earned a spot in the POEM-4 mission. Their payload, STeRG-P1.0, was designed to evaluate whether commonly used electronics could be used in place of expensive, space-grade components. The payload tested ARM processor-based avionics and MEMS sensors to perform attitude and inertial measurements—critical parameters for satellite orientation and motion tracking. The system included innovative data filtering algorithms and underwent rigorous in-house development over 38 weeks. The final design weighed 380 grams and measured 100 x 100 x 25 mm³, earning its place aboard ISRO's SpaDeX launch on December 30, 2024.

The success of STeRG-P1.0 highlights the critical role of electronics and communication in space systems, from sensors to embedded processing and data transmission. Additionally, future collaborations with ISRO are already in discussion, including proposals for a CubeSat mission and the development of STeRG-2.0.

With its innovative approach and strong academic backing, STeRG is poised to play a meaningful role in India's space technology journey.



Ms. Anushka Sapre
(Alumni-2019 passout)
Process Improvement
Manager,
Buoyancy Aerospace
Limited

Faculty Achievements



Prof. (Dr.) Pradeep V. Jadhav
Principal

1. Award for Student Engagement and Digital Innovation

Principal Prof. Dr. Pradeep Jadhav was honoured at the 9th Academic Leadership Summit and Awards in New Delhi on 21 February 2025 for his outstanding work in student engagement and digital innovation, recognizing his success in advancing digital learning, student participation, and academic excellence through technology-driven, student-centered teaching.

2. Advocacy for Academia–Industry Collaboration

At the She Inspires Hackathon 2025 held at Zensar, Kharadi, on 21 March 2025, Principal Prof. Dr. Pradeep V. Jadhav called for stronger links between academia and industry to equip students with real-world problem-solving skills, emphasizing equal opportunities, guidance, and support for women engineers.

3. International Outreach through Korea Edu Tour Roadshow

Guided by Principal Prof. Dr. Pradeep V. Jadhav, the college participated in the Korea Edu Tour Roadshow at Hyatt Regency, Pune, engaging in discussions on academic partnerships, student exchanges, and global networking to strengthen international collaboration between India and South Korea.

4. Principal of the Year Award at HEIT Summit 2025

On 15 May 2025, Principal Prof. Dr. Pradeep Jadhav received the Principal of the Year Award for Institutional Excellence at the 8th HEIT Summit and Awards in Pune, honouring his visionary leadership and commitment to academic quality, innovation, and institutional growth.

Department of Engineering Sciences and Allied Engineering



Prof. (Dr.) Avinash M. Pawar

Design Patent Granted on “Innovative Battery Cooling Device with Low Diverting Discs” (Design No. 454100-001, Registered on 03-04-2025)



Prof. (Dr.) Milind A. Patwardhan

1. Successfully completed Ph.D. on title “A Research on Comparative Study of High-Temperature Superconducting Magnetic Energy Storage System (SMES) Devices for Smart Grid Applications”, under the guidance of Dr. Vivek Yadav, from Sunrise University Alwar.

2. A design patent published on “AI based device for HR operations” on 7th February 2025 (Application No. 442488-001).



Prof. (Dr.) Smita S. Jadhav

A design patent published on “AI based predictive analytics of complex data sets in diagnosis of critical lung cancer in clinical traits” on 2nd May 2025 (Application No. 202541033428).



Prof. (Dr.) Maharudra K. Kapase

Successfully completed Ph.D. on title “A Critical Study of Major Themes in Kazuo Ishiguru’s Select Novels”, under the guidance of Dr. S. T. Haibatpure, from Swami Ramanand Teerth Marathwada University.

Department of Electronics & Telecommunication Engineering



Prof. Dr. S. R. Patil

BE Project group secured first prize in DIPEX-2025 (A State Level Exhibition cum Competition of working model) under his guidance for the topic “Accident avoidance system” held during 3rd to 6th April, 2025.



Prof. Dr. V. R. Pawar

Selected as a Board of Studies Member (Electronics & Telecommunication Engineering Department) at Trinity College of Engineering & Research., Pune.

A Candidate completed Doctorate of Philosophy under her guidance in “Anamolous activity detection using distributed deep learning model for intelligence video surveillance system” from SPPU on 24 April 2025.

A Candidate completed Doctorate of Philosophy under her guidance in “Automatic diabetic Retinopathy detection and classification using machine learning and texture features” from SPPU on 25 April 2025.

NPTEL online Certification course on subject “NBA Accreditation and Teaching & Learning in engineering (NATE)” passed with Elite certification.



Prof. Dr. S. A. Itkarkar

Successfully completed the degree of Doctorate of Philosophy in the topic “Evaluation of the current wavelength converter design framework and improve its capabilities for use in WDM network deployment” under the guidance of Prof. Dr. D.S Bhangari offered by Shridhar University.

A Patent published & granted on “Solar powered medicinal planar extractor” on 20 January 2025.



Prof. Dr. S. L. Kore

A Patent published & granted on “A novel multiclass-multistage writer verification using hybrid approach in in spatial domain and transform domain” on 7 March 2025.



Dr. S. A. Dhole

A Patent published & granted on “Solar powered medicinal planar extractor” on 20 January 2025.

A Patent published & granted on “Multimodal Biometric Identification System” on 14 February 2025.



Dr. S. M. Jagdale

A Patent published & granted on “Solar powered medicinal planar extractor” on 20 January 2025.



Dr. S. S. Salunkhe

A Book published on the topic “IoT and Its Application a hands on approach” by Scientific International publishing House on 01January 2025.

A Patent published & granted on “Semi-automatic solar powered pesticide sprayer for farming” on 02 April 2025.



Prof. P. R. Yawle

A Patent published on “Scanner for data Processing” on 15 January 2025.



Prof. R. J. Sapkal

Recognized as Oracle APEX Cloud developer certified professional by Oracle Corporation on 25 April 2025.



Dr. R. M. Shamalik

A Book published on the topic “Design, simulation and implementation of flexible micro strip antenna” by Lap Lambert Academic Publishing on 16 January 2025.

Department of Engineering Sciences and Allied Engineering
Students' Achievements (A. Y. 2024-25)

Sr. No.	Name of the Students Received Scholarship	Name of the Scholarship Received	Award (Item/Amount in Rs.)
1.	SHRADDHA NIRMAL	IEEE WIE	Laptop
2.	SHRADDHA RAMKISHAN NIRMAL	IEEE WIE	Laptop
3.	AKSHATA SANTOSH LALA	Katalyst	50,000/-
4.	BHAKTI VIJAY WARGHUDE	Katalyst	50,000/-
5.	BHAKTI VIJAY WARGHUDE	Katalyst	50,000/-
6.	DIKSHA BAPUSAHEB CHAVAN	Katalyst	50,000/-
7.	DNYANESHWARI SUNIL KHENAT	Katalyst	50,000/-
8.	DNYANESHWARI SUNIL KHENAT	Katalyst	50,000/-
9.	JANVHI RAJU HUGAR	Katalyst	50,000/-
10.	KHUSHI KAILAS DODE	Katalyst	50,000/-
11.	NIKITA JAKAPPA PUJARI	Katalyst	50,000/-
12.	PRATIKSHA BALASAHEB KINKAR	Katalyst	50,000/-
13.	SAKSHI VIJAY KHENGARE	Katalyst	50,000/-
14.	SAKSHI VIJAY KHENGARE	Katalyst	50,000/-
15.	SONALI SUNILKUMAR KAPURE	Katalyst	50,000/-
16.	SRUSHTI ARUN KADAM	Katalyst	50,000/-
17.	VAISHNAVI SHANKAR VITKAR	Katalyst	50,000/-
18.	PRIYANKA DHANANJAY ANBHULE	Katalyst	50,000/-
19.	SAMRUDDHI SANJAY POTEKAR	Katalyst	50,000/-
20.	PRIYANKA DHANANJAY ANBHULE	Lila Poonawalla	50,000/-
21.	AKSHATA SANTOSH LALA	Lila Poonawalla	60,000/-
22.	ARPITA KAUTIK PATIL	Lila Poonawalla	70,000/-
23.	CHETANA VINOD SHENAI	Lila Poonawalla	50,000/-

24.	GAURI CHILWANT	Lila Poonawalla	62,000/-
25.	GAYATRI KANTILAL KHADE	Lila Poonawalla	70,000/-
26.	KOKARE APEKSHA PINUKDEV	Lila Poonawalla	70,000/-
27.	NIVEDITA JHA	Lila Poonawalla	70,000/-
28.	PRATIKSHA BALASAHEB KINKAR	Lila Poonawalla	70,000/-
29.	PURVA VISHNU UPADHYAY	Lila Poonawalla	50,000/-
30.	RUCHA SHANKAR DESAI	Lila Poonawalla	50,000/-
31.	SANIKA BANDU KARDULE	Lila Poonawalla	70,000/-
32.	SANIKA BANDU KARDULE	Lila Poonawalla	70,000/-
33.	SAYYED ADIBA BASHIR	Lila Poonawalla	70,000/-
34.	SHAGUN BHARAT BAWANKAR	Lila Poonawalla	70,000/-
35.	SHARWARI YOGESH CHAVAN	Lila Poonawalla	70,000/-
36.	SIDDHI LAXMAN AGALE	Lila Poonawalla	70,000/-
37.	VASUNDHARA AVADHUT PATIL	Lila Poonawalla	70,000/-
38.	VEDIKA HEMANT PHATAK	Lila Poonawalla	35,000/-
39.	SAMRUDDHI SANJAY POTEKAR	Lila Poonawalla	70,000/-

Department of Electronics & Telecommunication Engineering

- The BE E&TC project group, **Ms. Annaya Wagh, Ms.Nikta Wadhude and Ms.Snehal Shinde** has won First prize in DIPEX-2025, A State Level Exhibition cum Competition of working model under the guidance of Prof. Dr. S. R. Patil organized by College of Engineering on 3rd to 6th April 2025, and Pune.
- The BE E&TC project group, **Ms. Gatatri Yeole, Ms. Tanushree Velapure and Ms.Sakshi Talekar** has won First prize in IETE Project Competition cum exhibition under the guidance of Prof. M. S. Kasar organized by MES Wadia collge of Engineerimng on 1st April to 5th April 2025, Pune.
- The BE E&TC project group, **Ms. Sakshi Sheth and Ms. Janhvi Sarode** has won Third prize in National conference IAET-25 at Bharatiyugam under the guidance of Prof. S.M. Thorat organized by BVCOEW on 8th April 2025, Pune.
- A TE student, **Ms. Vedika Rajemane** was selected for National Republic Day Parade 2025 at New Delhi.

Our Esteemed Recruiters



Reliance



Placements from December 2024 to May 2025

Sr. No.	Name of the Student	Company
1	APURVA DEEPAK DANDGAVHAL	COGNIZANT
2	SHRUTI PRABHUDAS NAGE	COGNIZANT
3	RIDDHI SHAILESH BALDWA	PLANET SPARK
4	PRACHI BANSIDHAR KALE	SILVER
5	TANUSHREE PANDURANG VELAPURE	COGNIZANT
6	NISHIGANDHA NILKANTH GOBADE	EXENITY EXCELLENCE INFINITE
7	PRIDIL DINESH SONJE	EXENITY EXCELLENCE INFINITE
8	NISHIGANDHA NILKANTH GOBADE	EXENITY EXCELLENCE INFINITE
9	KAJAL KARAD	WIPRO

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